

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta et al. Art Unit: 2189  
Serial No. : 10/776,702 Examiner: Behzad Peikari  
Filed : February 10, 2004  
Assignee : Intel Corporation  
Title : SRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE  
INCLUDING A READ QUEUE AND AN ORDER QUEUE FOR  
HANDLING REQUESTS (AS AMENDED)

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Notice of Allowance Date: August 1, 2007

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed August 1, 2007, enclosed is a completed issue fee transmittal form PTOL-85b.

COMMENTS ON EXAMINER'S REASONS FOR ALLOWANCE

It is agreed that the limitations recited in the examiner's Reasons for Allowance are not taught or suggested by the art of record, and that the allowed independent claims 18, 26, and 33 are distinguished from the cited prior art for at least the reasons stated in the Reasons for Allowance. Applicant does not concede that the stated reasons are the only grounds for patentability of the allowed claims, that the limitations

Applicant : Matthew J. Adiletta et al. Attorney's Docket No.: 10559-078003  
Serial No.: 10/776,702 P7570C2  
Filed : February 10, 2004  
Page : 2

excluded from the Reasons for Allowance are taught or suggested by the art of record, or that all of the limitations are necessary for patentability of the allowed claims or other claims directed to the disclosed subject matter. For example, other claims including different limitations are patentable over the cited prior art.

Please apply the required fees in the amount of \$1,740 to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: October 24, 2007

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